

Analog Bits Expands Global Engineering Footprint with New Design Center in Dongtan, South Korea

Strategic investment strengthens worldwide engineering capabilities and accelerates innovation to support growing need for smarter and more efficient analog and power solutions for advanced semiconductors.

Sunnyvale, CA, July 15th, 2026 – Analog Bits (www.analogbits.com), the leading provider of low-power mixed-signal IP (intellectual property) solutions, has expanded its engineering capacity with a new design center in Dongtan, South Korea. This follows the expansion of the company's Prague design center in 2024, a continuation of the company's global growth strategy to provide highly experienced engineering design team support in key locations around the world to meet the growing need for smarter and more efficient analog and power solutions for the advanced semiconductor market.

The Dongtan design center's proven team has experience down to 2nm technologies for AI and other applications and strengthens the company's ability to develop next-generation analog and mixed-signal IP while providing closer collaboration and support for customers across Asia and around the world. "This milestone of expansion is strategic for our future growth and commitments to our customers," said Mahesh Tirupattur, CEO at Analog Bits. "By expanding our global engineering organization with exceptional talent, we are increasing our capacity to innovate, accelerate product development, and deliver the high-quality IP solutions our customers depend on for their next generation AI, networking, automotive, industrial, and high-performance computing devices."

Power is often the biggest challenge in system-on-chip (SoC) design today, a gating constraint within today's largest AI and HPC systems which operate at kilowatt-per-SoC and megawatt-per-rack power scales. This applies universally, from demand for compute in data centers and advanced driver assistance systems, to minimizing power consumption in IoT and wearable devices. A key part of addressing this challenge is to enable chip designs that intelligently manage power demands directly within an SoC, so that designers can continuously deliver on power targets while meeting the performance required of the end product. The industry consensus is that the winners of the AI chip era will be determined by power efficiency, thermal management, and signal integrity at high data rates — not raw compute density alone.

According to Tirupattur, “Our mixed signal IP often sits at the intersection of all three, determining how efficiently power is delivered on chip, how accurately signals are timed, and how cleanly high-speed data moves through interconnects. This is where close collaboration with customers and foundries becomes critical, and our design centers in Prague, Czechia, and now Dongtan in South Korea, helps Analog Bits’ position fulfil this commitment to our global customers and partners.”

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About Analog Bits:

Founded in 1995, Analog Bits, Inc. is the leading supplier of mixed-signal IP with a reputation for easy and reliable integration into advanced SOCs. Its products include precision clocking macros, sensors, programmable interconnect solutions such as multi-protocol SERDES and programmable I/Os. With billions of IP cores fabricated in customer silicon, from 0.35um to 2nm processes, Analog Bits has a strong track record of first-time-working silicon with foundries and integrated device manufacturers.