

Analog Bits Demonstrates Real-Time On-Chip Power Sensing and Delivery on TSMC N2P Process at TSMC 2026 Technology Symposiums

Analog Bits continues to develop innovative mixed signal IP in TSMC N2P process technology, with integrated LDOs, pinless remote PVT sensors, and microwatt class and high-performance LC PLLs

Sunnyvale, CA, April 15th, 2026 – Analog Bits (www.analogbits.com), developer of industry leading low-power mixed-signal IP (intellectual property) solutions for intelligent power and energy management, will showcase new integrated on-die LDOs with glitch catcher and droop detection, pinless PVT sensors, and low power PLLs that deliver comprehensive real-time power observability and sensing in TSMC’s N2P process technology.

Modern Megawatt AI and HPC systems using Multi Kilowatt SoC’s face thermal, power efficiency, performance variability and reliability challenges that digital design alone cannot solve. Moore’s law is no longer power friendly with transistor speeds going up and voltage levels not scaling down causing more power density challenges. Customers leverage advanced process and packaging energy-efficiency techniques along with architectural-level optimizations to achieve their power targets.

The new IPs will provide significant advantages to customers seeking PPA optimization and intelligent on-chip power management for their advanced SoCs on TSMC N2P process technology. Mahesh Tirupattur, CEO of Analog Bits, commented, “Our integrated on-die LDO provide an exceptionally clean form of power delivery, coupled with the glitch catcher and droop detector features, making power observable in real-time and enabling fast corrective actions to be taken almost instantaneously.”

Also being featured for the first time are the highly accurate remote pinless PVT sensors with a +/-3.5C (untrimmed), and low power PLLs that feature microwatt class power levels at 0.5 micro-watt/MHz.

Visit our booth and see a demo of over 12 IPs on the TSMC N2P test-chips.

When

April 22, 2026

Register at:

<https://www.tsmc.com/static/english/campaign/Symposium2026/index.htm>

Location

Santa Clara Convention Center, Booth #608

The company will also be participating at the TSMC Technology Symposiums in Taiwan, Europe, China, and Japan.

Analog Bits, Inc. is the established supplier of an industry-leading portfolio of mixed-signal IP for intelligent energy and power management, with a reputation for easy and reliable integration into advanced SoCs. Its products include precision clocking macros, power and temperature sensors including LDO and regulators, programmable interconnect solutions such as multi-rate SERDES and programmable I/O's. With billions of IP cores fabricated in customer silicon, from 0.35 micron to 2nm processes, Analog Bits has an outstanding heritage of first-time-working IPs that lower risk. For more information and a detailed product selection guide, visit www.analogbits.com.

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