

Analog Bits Showcases Silicon of Analog and Mixed Signal IP Products on TSMC N7 Process Targeting Automotive Grade with Split Corner Lots and PVT Characterization Results Available

Highlights

- Analog Bits is demonstrating its silicon results targeting automotive grade for its analogand mixed signal IP products on TSMC's N7 process. The list of IPs includes wide range Integer PLL, Fractional PLL, Sensors, differential high speed IOs and Wide Frequency Range Oscillator Macro.
- The complete family of analog and mixed-signal IPs, now with characterizationreport is immediately available for customer integration and tape-out.

Santa Clara, CA, September 26th, 2019 – Analog Bits (www.analogbits.com), an industry leading provider of low-power mixed-signal IP (Intellectual Property) solutions is demonstrating its silicon results targeting automotive grade for its analog and mixed signal IP products on TSMC's N7 process. The list of IPs includes wide range Integer PLL, Fractional PLL, Sensors, differential high speed IOs and Wide Frequency Range Oscillator Macro.

What

7nm test chip silicon measurement targeting automotive grade:Highlights of the Demo:

- 8 GHz PLLs for Integer and FracN
- 4 Gbps TX and RX IO's
- Wide Frequency Range Oscillator Macro
- Integrated Sensors

When

September 26, 2019 (registration begins at 8:00am)

Where

TSMC 2019 Open Innovation Platform® Ecosystem Forum, Booth: 908, Santa ClaraConvention Center, 5001 Great America Parkway, Santa Clara, CA 95054

NEWS ALERT



About Analog Bits

Founded in 1995, Analog Bits, Inc. (www.analogbits.com), is the leading supplier of mixed-signal IP with a reputation for easy and reliable integration into advanced SOCs. Products include precision clocking macros such as PLLs & DLLs, programmable interconnect solutions such as multi-protocol SERDES and programmable I/O's as well as specialized Sensors.

With billions of IP cores fabricated in customer silicon, from 0.35-micron to 7-nm processes, Analog Bits has an outstanding heritage of "first-time-working" with foundries and IDMs.

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