

Low power PCI Gen4/5 SERDES on Samsung 8LPP

- ✓ Silicon Proven PCIe Gen4/5 IP in 8LPP
- ✓ 32G performance demonstrated on Samsung 8LPP
- ✓ PIPE compliant PHY integrates easily with industry standard controllers



SERDES

Low Power and
Optimized for
Performance PCIe
SERDES



Clocking

PLL IP's – Integer/FracN
PLL and Ref Clock PLL's
available upon request



Sensors

Sensor IP's – PVT Sensor
available and Power
Supply Sensors available
upon request