

Low power PCI Gen3/4 SERDES on Samsung 14LPP

- ✓ Production Proven PCIe Gen3/4 IP in Samsung 14LPP
- ✓ PIPE compliant PHY integrates easily with industry standard controllers
- ✓ Low Latency best suited for PCIe Re-timer application



SERDES



Clocking



Sensors

Low Power and
Optimized Enterprise
class PCIe4 SERDES

Production proven
Integer PLL IP's

Ref Clock PLL's available
upon request

Production Proven PVT
Sensor IP's and Power
Supply Sensors available
upon request