



Silicon Proven IP's in 14LPP and 12LP

- ✓ Customer Silicon Production in 14LPP
- ✓ Test Chip Silicon Proven in 12LP






Clocking

-  Wide-Range Integer FracN PLL with SSCG
-  PCIe Gen2/3 Ref Clock Generator with SSCG

Sensors



-  PVT Sensor
-  P Only Sensor
-  POR Macro with OCD Detection