

Broad Portfolio of Analog Foundation IP in Global Foundries 12LP+

- ✓ Silicon proven IP available in Q1-2021
- ✓ Rapid migration of 12LP IP's and more



Clocking



Wide-Range Integer
FracN PLL with SSCG



PCIe Gen2/3 Ref Clock
Generator with SSCG



PCIe Gen4/5 Ref Clock
Generator with SSCG

Sensors



PVT Sensor



POR Macro with
OCD Detection