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**Analog Bits Low Jitter LC Tank PLL IP Targets Ultra High End Networking,
Cloud Computing SoC's**

*Less Than 300fs-RMS Random Jitter performance achieves "next level"
precision*

Mountain View, Calif., August 2nd, 2011 – Analog Bits, the Integrated Clocking and Interface IP leader, today announced the immediate availability of a new high-precision, low jitter LC Tank PLL IP for System-on-Chip (SoC) applications running at 100 Gigabits per second (Gbps) and above data transfer rates.

This new addition expands Analog Bits' Clocking IP product line of PLLs which will now enable SoC designs for very high-end telecommunications and networking applications that are helping expand the cloud computing market.

Positioned for high fidelity, low jitter sampling and transmission applications, the new LC Tank PLL operates on less than 12 milliwatts (mW) while capable of providing multiphase clocks of up to 14 Gigahertz (GHz) in a footprint tinier than 0.26 square millimeters (mm).

"We applaud Analog Bits for achieving extremely low jitter with its newest LC Tank PLL IP products. The LC Tank PLL IP will enable 28 gigabits per second SerDes for the rapidly growing 100 GigaBit Ethernet applications," said Norman Yeung, Vice President of Engineering , Inphi Corporation.

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The new LC Tank PLL is silicon prove in TSMC's 40nm General Purpose (G) process and is actively being ported to the foundry's 28nm node. It incorporates special process components, such as process-specific inductors, to achieve its very high precision.

“To achieve better than 300 femtosecond performance is definitely a clocking IP milestone. Putting this in context, 300 femtoseconds is the duration of a vibration of the atoms in an iodine molecule, and a femtosecond is to a second, what a second is to 31.7 million years”, explains Mahesh Tirupattur, Executive Vice President, Analog Bits

For information visit www.analogbits.com or email: info2011@analogbits.com

About Analog Bits: Founded in 1995, Analog Bits, Inc. is the leading supplier of low-power, customizable analog IP for easy and reliable integration into modern CMOS digital chips. Our products include precision clocking macros such as PLL's & DLL's, programmable interconnect solutions such as multi-protocol SERDES/PMA and programmable I/O's as well as specialized memories such as high-speed SRAMs and TCAMs. With billions of uses of IP fabricated in customer silicon from 0.35-um to 28-nm processes, Analog Bits is the premier analog IP supplier with an outstanding heritage of "first time working silicon" at merchant foundries and IDMs.